

U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICANTS

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TITLE

Differential CMOS controlled delay unit

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER 	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. 	_____ (Assistant Examiner) (Date) _____ (Primary Examiner) (Date) _____ (Legal Instruments Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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